

MARK THOREN | LINEAR TECHNOLOGY CORP. mthoren@linear.com

PWM-Based Analog Calculator Provides Four-Quadrant Multiplication, Division

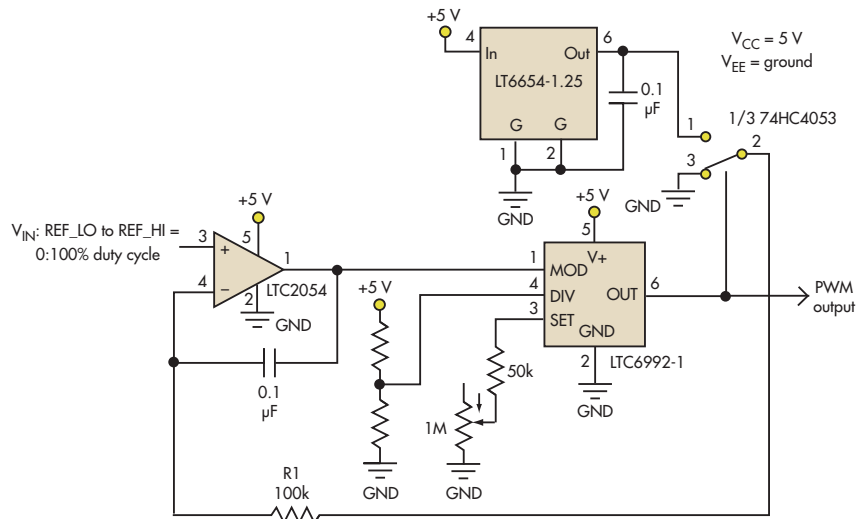
YOU CAN BUILD an analog calculator using a pulse-width modulator (PWM) to perform accurate four-quadrant multiplication and division. While this approach won't help you ace any math tests, it demonstrates some useful sub-circuits that extend the functionality of the LTC6992 TimerBloX voltage-controlled PWM.

The LTC6992-1 translates a 0-1 V input at the MOD pin into an output with a 0% to 100% duty cycle at a frequency of 3.81 Hz to 1 MHz. A resistor at the SET pin and a resistor divider at the DIV pin control this frequency. In some applications, the LTC6992 will be in the feed-forward path of a closed-loop control system (as in a motor-speed controller), so its 1% typical linearity provides consistent overall loop performance.

Figure 1 shows a basic linearized PWM generator for applications where accurate PWM is required without an external feedback mechanism. This circuit easily achieves 0.1% PWM accuracy. The output of the LTC6992 controls one section of a 74HC4053 triple single-pole double-throw (SPDT) analog switch whose output is switched between ground and an LT6654-1.25 reference. An integrator compares this signal to the control input. The output duty cycle will settle on a value that equals the fraction of the 1.25-V reference that's present at the input. The term "fraction" implies that this circuit performs division, as the output PWM duty cycle is V_{IN}/V_{REF} .

Figure 2 extends this concept, with X as the input (numerator) and Y as the reference (denominator). An LT1991 configured in a gain of -1 provides a precise negative copy of Y, extending operation to four quadrants (positive and negative X and Y), with duty cycle = $50\% \times [1 + (X/Y)]$.

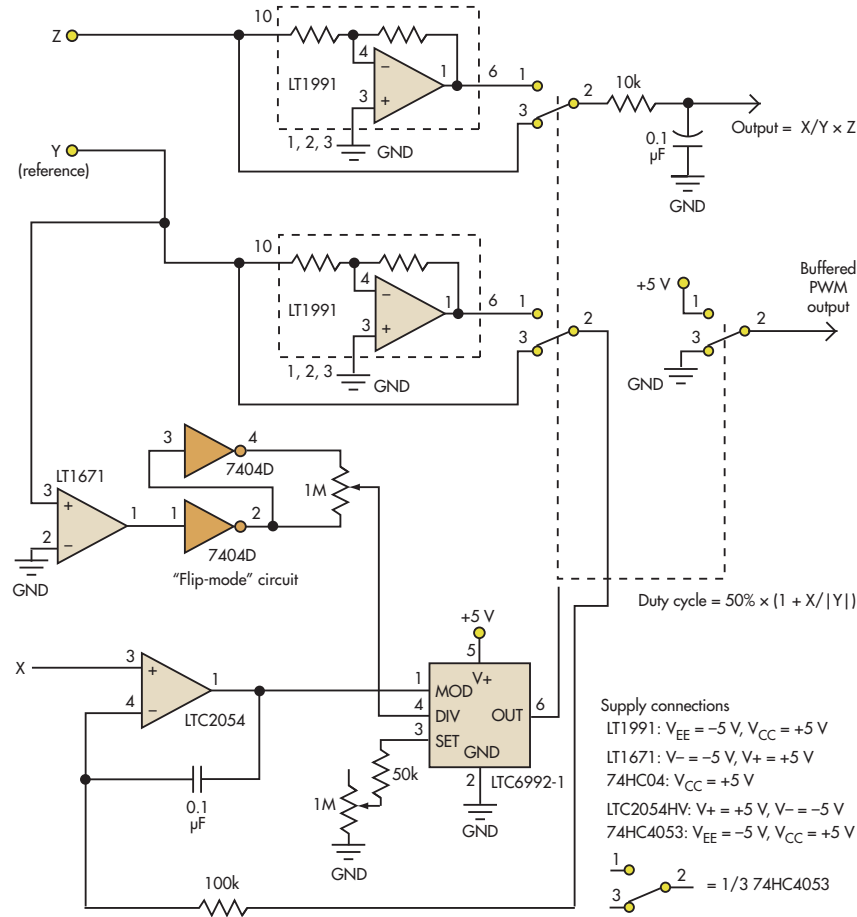
As with any physical realization of division, a zero value for the denominator (Y) will produce an undefined output. A negative voltage applied to the Y input inverts the polarity of the feedback signal to the integrator, which requires another inversion



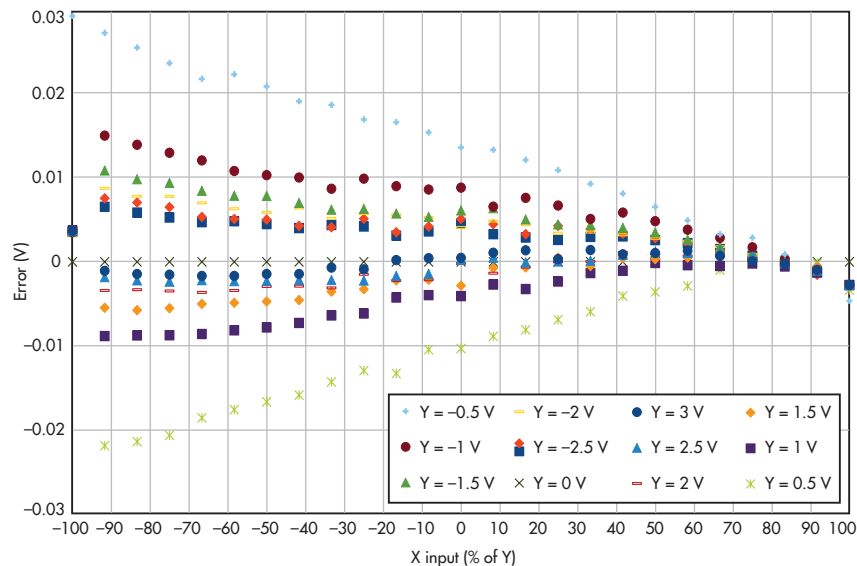
1. This basic linearized PWM generator, without an external feedback mechanism, can still provide accuracy of 0.1%.

DIVCODE PROGRAMMING

DIVCODE	POL	N _{DIV}	Recommended f _{OUT}	R1 (kΩ)	R2 (kΩ)
0	0	1	62.5 kHz to 1 MHz	Open	Short
1	0	4	15.63 to 250 kHz	976	102
2	0	16	3.906 to 62.5 kHz	976	182
3	0	64	976.6 Hz to 15.63 kHz	1000	280
4	0	256	244.1 Hz to 3.906 kHz	1000	392
5	0	1024	61.04 to 976.6 Hz	1000	523
6	0	4096	15.26 to 244.1 Hz	1000	681
7	0	16384	3.815 to 61.04 Hz	1000	887
8	1	16384	3.815 to 61.04 Hz	887	1000
9	1	4096	15.26 to 244.1 Hz	681	1000
10	1	1024	61.04 to 976.6 Hz	523	1000
11	1	256	244.1 Hz to 3.906 kHz	392	1000
12	1	64	976.6 Hz to 15.63 kHz	280	1000
13	1	16	3.906 to 62.5 kHz	182	976
14	1	4	15.63 to 250 kHz	102	976
15	1	1	62.5 kHz to 1 MHz	Short	Open



2. This enhancement to the circuit of Figure 1 extends the analog multiplication/division to all four analog signal quadrants.



3. This plot of absolute error shows better than 0.1% for large values of Y.

somewhere in the loop to ensure feedback is negative.

The DIV pin also can invert the PWM polarity (a 0- to 1-V input = 100% to 0% duty-cycle output), in addition to selecting one of eight N_{Div} values to set the frequency. The N_{Div} magnitudes are mirrored around $V_{CC}/2$, where swapping values in the resistor divider inverts the transfer function while maintaining the same divider value (see the table).

An LT1671 comparator detects the polarity of the Y input and sets the polarity by switching the divider potentiometer's excitation accordingly, maintaining correct operation. Note that a 10-turn potentiometer works well for experimentation. You could replace it with a fixed resistor once you have selected the desired N_{Div} .

The “Z” input is multiplied by the X/Y quotient by supplying the inputs to another switch with Z and -Z. (Once again, an LT1991 provides precision inversion.) This is a “pulse width/pulse height” multiplier, also with four-quadrant operation.

Figure 3 shows the absolute error of the circuit at a 1.5-kHz frequency, sweeping X from -Y to +Y for values of Y from -3 V to +3 V, while holding Z constant at 5 V. Even with Y at 0.5 V (where error sources are more significant), the worst-case error is about 0.6% and rapidly improves with larger values of Y. Error sources include the 0.04% error of the LT1991, mismatch in switch resistance between its two positions compared with the resistance of the downstream filter's resistance, and the response of the LT1991 outputs to switching transients, whose effect will vary with PWM frequency.

MARK THOREN is the applications engineering manager for mixed-signal products at Linear Technology. He has a BS in agricultural/mechanical engineering and an MS in electrical engineering, both from the University of Maine.



Driven Shield Enables Large-Area Capacitive Sensor

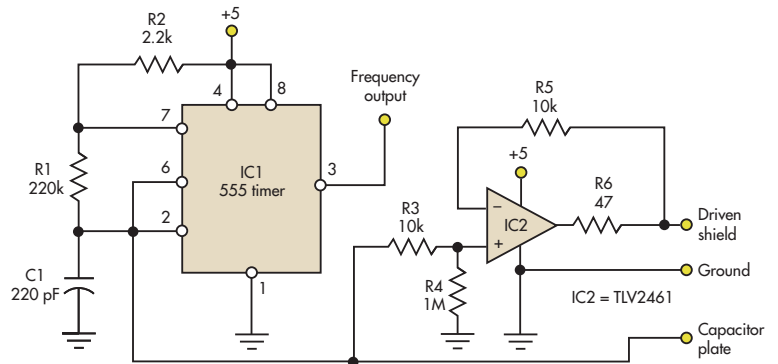
CAPACITIVE SENSORS ARE common in today’s consumer electronics, since they are used in many touchscreen applications. Most of the circuits for such applications are designed for small-area capacitors and finger contact operation.

This circuit is designed for large-area touch plates that can be activated at a distance and hidden behind walls or inside other structures. It uses the “driven shield” concept that was popular in the early days of audio engineering, when high-impedance sound sources such as crystal microphones were used. Shielded cables are needed to connect these devices, but the cable capacitance limits the high-frequency response.

The solution to this problem is to go back to the physics of a capacitor to find a way to reduce the cable capacitance. Driving a conductor between the outer shield and the inner, signal conductor with the same voltage as the signal conductor largely eliminates the capacitance. NASA used this driven-shield idea in a large-area capacitive sensor two decades ago (John M. Vranish, et al., US Patent No. 5,116,679).

The circuit in Figure 1 performs the main capacitance-sensing task, while leaving detection to a microcontroller. IC1 is a 555 timer wired as an astable multivibrator with as small a timing capacitance as possible. With the component values shown, the frequency is slightly above 10 kHz. IC2 buffers the voltage on this capacitor to drive a shield plate.

For IC2, resistors R3 and R4 decrease the buffered voltage by about 1%. This prevents oscillation that may occur because of the external capacitance. R6 is included since some operational amplifiers have difficulty driving a capacitive load. IC2

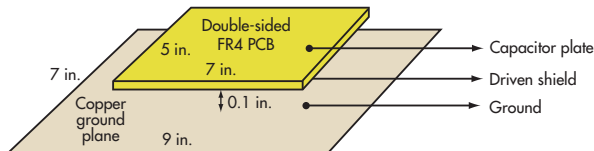


1. In this large-area capacitive sensor, C1 and any parallel capacitance set the frequency of the 555 astable multivibrator IC1, while IC2 buffers the voltage at the capacitor to drive the shield plate. (The pin-outs for IC1 are for the “N” package.)

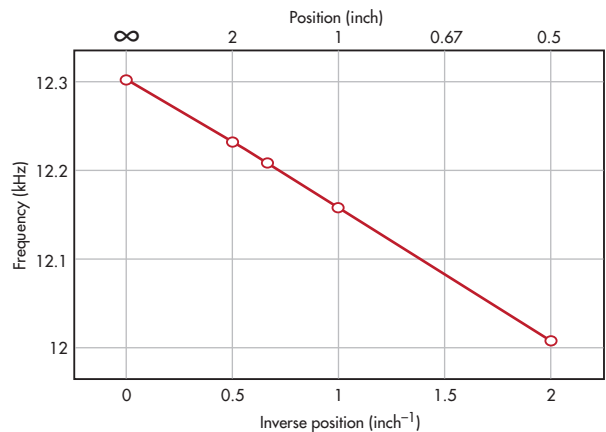
should have a high gain-bandwidth product to allow a faithful representation of the signal voltage without phase shift.

Figure 2 shows the construction of the sensing plate. These are the dimensions used in the author’s tests, but quite a lot of variation is permissible. A practical implementation would be to build the circuitry onto an edge of the ground plane.

The frequency response decreases in a nearly linear fashion with respect to inverse position (Fig. 3). In the author’s test, hand contact reduced the frequency to 10.47 kHz. Hand contact through a 0.375-in. (10 mm) thick piece of plywood or gypsum wallboard resulted in a frequency change of 10%, which is easy to detect. Although frequency-detection ICs are available, a simpler, less expensive solution is to use a microcontroller to detect a change in signal period. 📺



2. In the constructed sensor, a double-sided printed-circuit board (PCB) has one conductor as the signal plate and the other as the driven shield. The semi-infinite ground plane, which is slightly larger in area than the sensing or shield plates, can be another PCB or an aluminum plate.



3. The circuit response is a close-to-linear inverse-position relationship between hand position and the sensing plate.

DEV GUALTIERI received his PhD in solid-state science from Syracuse University in 1974. He now does various computer, electronic, and embedded systems projects at his consulting company, Tikalon LLC.

